

PATENT APPLICATION
042390.P3275RC1

adjusting the voltage potential level provided to at least a portion of the computing platform based on the application mix executed by the computing platform.

25. (Twice amended) An apparatus, comprising:
a processor;

a voltage regulator adapted to provide at least two voltage potential levels to at least a portion of the processor; and

wherein the voltage potential level provided by the voltage regulator is adapted to be adjusted depending on the operational load of the processor, and is provided to an electrically common terminal in the processor.

32. (Newly added) A method comprising:

determining a frequency for an application mix and providing a voltage potential for the frequency.

33. (Newly added) The method of claim 32, wherein providing a voltage potential includes providing a voltage potential to a core of a processor.

34. (Newly added) The method of claim 33, where in providing a voltage potential includes providing a different voltage potential to a pad of the processor.

35. (Newly added) An apparatus comprising:

a state machine adapted to determine a frequency for an application mix, wherein the state machine is further adapted to provide a voltage potential to at least a core of a processor for the frequency.

36. (Newly added) The apparatus of claim 35, further comprising a voltage regulator to provide the voltage potential level.

37. (Newly added) The apparatus of claim 35, wherein the processor is adapted to receive a clock signal that is varied in accordance with changes in the application mix of the processor.